

CLAIMS:

1. A method for forming a metal layer, comprising the steps of:
 - (a) forming a sacrificial metal atomic layer on a semiconductor substrate;
 - (b) removing the sacrificial metal atomic layer and concurrently forming a metal atomic layer on the semiconductor substrate by reacting the sacrificial metal atomic layer with a metal halide gas; and.
 - (c) stacking a plurality of metal atomic layers on the semiconductor substrate by alternately forming the sacrificial atomic layer and the metal atomic layer, at least once.
- 5 2. The method of claim 1, further comprising the step of forming an initial sacrificial metal layer on the semiconductor substrate before step (a) of forming the sacrificial metal atomic layer.
- 10 3. The method of claim 2, wherein while the initial sacrificial metal layer is formed, the semiconductor substrate is heated to 300 ~ 500°C.
- 15 4. The method of claim 2, wherein the initial sacrificial metal layer is formed of the same material as the sacrificial metal atomic layer.
5. The method of claim 2, wherein the initial sacrificial metal layer is formed using the same reaction gas as the reaction gas used for forming the sacrificial metal atomic layer.
6. The method of claim 1, wherein while the sacrificial metal atomic layer and the 20 metal atomic layer are formed, the semiconductor substrate is heated to 300 ~ 500°C.

7. The method of claim 1, wherein the Gibbs free energy of a composition including a metal atom of the sacrificial metal atomic layer and a halogen atom of the metal halide gas is higher than that of the metal halide.

8. The method of claim 7, wherein the sacrificial metal atomic layer is formed by
5 reacting a sacrificial metal source gas and a reducing gas with each other.

9. The method of claim 8, wherein the reducing gas is one selected from the group consisting of H₂ gas and silane gas.

10. The method of claim 7, wherein the metal halide gas is one selected from the group consisting of TiCl₄ gas, a TaCl₅ gas, HfCl₄ gas, ZrCl₄ gas, TiI₄ gas, TaI₅ gas, HfI₄ gas, ZrI₄ gas, TiBr₄ gas, TaBr₅ gas, HfBr₄ gas, ZrBr₄ gas, TiF₄ gas, TaF₅ gas, HfF₄ gas and ZrF₄ gas.
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11. The method of claim 10, wherein when the metal halide gas is TiCl₄ gas, the sacrificial metal layer is one selected from the group consisting of an Al layer, a La layer, a Pr layer, an In layer, a Ce layer, a Nd layer and a Be layer.

12. The method of claim 11, wherein the sacrificial metal source gases used for the Al layer, the La layer, the Pr layer, the In layer, the Ce layer, the Nd layer and the Be layer are precursors containing Al, La, Pr, In, Ce, Nd and Be, respectively.

13. The method of claim 12, wherein the precursor containing Al is one selected from the group consisting of $(C_4H_9)_2AlH$, $(C_4H_9)_3AlH$, $(C_2H_5)_3Al$, $(CH_3)_3Al$, $AlH_3N(CH_3)_3$, $(CH_3)_2AlH$, and $(CH_3)_2C_2H_5N:AlH_3$.
14. The method of claim 12, wherein the precursor containing La is one selected from the group consisting of $(C_5H_5)_3La$ and $(C_2H_7C_4H_4)_3La$.
15. The method of claim 12, wherein the precursor containing Pr is one selected from the group consisting of $(C_5H_5)_3Pr$ and $(C_5H_7C_5H_4)_3Pr$.
16. The method of claim 12, wherein the precursor containing In is one selected from the group consisting of C_2H_5In , $(CH_3)_3C_5In$, $(C_2H_5)_3In$ and $(CH_3)_3In$.
17. The method of claim 12, wherein the precursor containing Ce is one selected from the group consisting of $(C_5H_5)_3Ce$ and $((C_5H_5)C_5H_4)_3Ce$.
18. The method of claim 12, wherein the precursor containing Nd is one selected from the group consisting of $(C_5H_5)_3Nd$ and $(C_5H_7C_5H_4)_3Nd$.
19. The method of claim 12, wherein the precursor containing Be is $Be(C_2H_5)_2$.
20. The method of claim 2, further comprising the step of purging the peripheral portion of the resultant structure where the initial sacrificial metal layer is formed, with an inert gas, before step (a) of forming the sacrificial metal atomic layer.

21. The method of claim 1, further comprising the step of purging the peripheral portion of the resultant structure where the sacrificial metal atomic layer is formed, with the inert gas, before step (b) of forming the metal atomic layer.

22. The method of claim 20, wherein the inert gas is one selected from the group consisting of N₂ gas and Ar gas.

23. The method of claim 21, wherein the inert gas is one selected from the group consisting of N₂ gas and Ar gas.

24. The method of claim 1, further comprising the step of forming an ohmic layer at the interface between the semiconductor substrate and the plurality of metal atomic layers, by reacting the plurality of metal atomic layers and the semiconductor substrate with each other using an annealing process after step (c) of stacking the plurality of metal atomic layers.

25. The method of claim 24, wherein during the annealing, an ambient gas is one selected from the group consisting of Ar gas, N₂ gas and NH₃ gas.

26. The method of claim 24, wherein the ohmic layer is a metal silicide layer.

27. A method for forming a metal silicide layer, comprising the steps of: forming a sacrificial metal atomic layer on a semiconductor substrate; removing the sacrificial metal atomic layer and at the same time forming a metal atomic layer on the

semiconductor substrate by reacting the sacrificial metal atomic layer with a metal halide gas;

forming a silicon atomic layer on the metal atomic layer; and alternately stacking a plurality of metal atomic layers and a plurality of silicon atomic layers on the semiconductor substrate by forming the sacrificial metal atomic layer, the metal atomic layer and the silicon atomic layer in sequence at least once.

28. The method of claim 27, further comprising the step of forming an initial sacrificial metal layer on the semiconductor substrate before the step of forming the sacrificial metal atomic layer.

29. The method of claim 28, wherein while the initial sacrificial metal layer is formed, the semiconductor substrate is heated to 300 ~ 500°C.

30. The method of claim 28, wherein the initial sacrificial metal layer is formed of the same material as that of the sacrificial metal atomic layer.

31. The method of claim 28, wherein the initial sacrificial metal layer is formed using the same reaction gas as the reaction gas used for forming the sacrificial metal atomic layer.

32. The method of claim 27, wherein while the sacrificial metal atomic layer, the metal atomic layer and the silicon atomic layer are formed, the semiconductor substrate is heated to 300 ~ 500°C.

33. The method of claim 27, wherein the Gibbs free energy of a composition consisting of a metal atom of the sacrificial metal atomic layer and a halogen atom of the metal halide gas is higher than that of the metal halide.

34. The method of claim 33, wherein the sacrificial metal atomic layer is formed by reacting a sacrificial metal source gas with a reducing gas.

35. The method of claim 34, wherein the reducing gas is one selected from the group consisting of H₂ gas and silane gas.

36. The method of claim 35, wherein the metal halide gas is one selected from the group consisting of TiCl₄ gas, TaCl₅ gas, HfCl₄ gas, ZrCl₄ gas, TiI₄ gas, TaI₅ gas, HfI₄ gas, ZrI₄ gas, TiBr₄ gas, TaBr₅ gas, HfBr₄ gas, ZrBr₄ gas, TiF₄ gas, TaF₅ gas, HfF₄ gas and ZrF₄ gas.

37. The method of claim 36, wherein when the metal halide gas is TiCl₄ gas, the sacrificial metal layer is one selected from the group consisting of an Al layer, a La layer, a Pr layer, an In layer, a Ce layer, a Nd layer and a Be layer.

38. The method of claim 37, wherein sacrificial metal source gases used to form the Al layer, the La layer, the Pr layer, the In layer, the Ce layer, the Nd layer and the Be layer are precursors containing Al, La, Pr, In, Ce, Nd and Be respectively.

39. The method of claim 38, wherein the precursor containing Al is one selected from the group consisting of $(C_4H_9)_2AlH$, $(C_4H_9)_3AlH$, $(C_2H_5)_3Al$, $(CH_3)_3Al$, $AlH_3N(CH_3)_3$, $(CH_3)_2AlH$ and $(CH_3)_2C_2H_5N:AlH_3$.

40. The method of claim 38, wherein the precursor containing La is one selected from the group consisting of $(C_4H_9)_3La$ and $(C_2H_7C_4H_9)_3La$.

41. The method of claim 38, wherein the precursor containing Pr is one selected from the group consisting of $(C_5H_5)_3Pr$ and $(C_3H_7C_5H_4)_3Pr$.

42. The method of claim 38, wherein the precursor containing In is one selected from the group consisting of C_2H_5In , $(CH_3)_2C_5In$, $(C_2H_5)_3In$ and $(CH_3)_3In$.

43. The method of claim 38, wherein the precursor containing Ce is one selected from the group consisting of $(C_5H_5)_3Ce$ and $((C_5H_5)C_5H_4)_3Ce$.

44. The method of claim 38, wherein the precursor containing Nd is one selected from the group consisting of $(C_5H_5)_3Nd$ and $(C_3H_7C_5H_4)_3Nd$.

45. The method of claim 38, wherein the precursor containing Be is $Be(C_2H_5)_2$.

46. The method of claim 28, further comprising the step of purging the peripheral portion of the resultant structure where the initial sacrificial metal layer is formed, with an inert gas, before the step of forming the sacrificial metal atomic layer.

47. The method of claim 27, further comprising the step of purging the peripheral portion of the resultant structure where the sacrificial metal atomic layer is formed, with an inert gas, before the step of forming the metal atomic layer.

48. The method of claim 27, further comprising the step of purging the peripheral portion of the resultant structure where the metal atomic layer is formed, with an inert gas, before the step of forming the silicon atomic layer.

49. The method of claim 46, wherein the inert gas is one selected from the group consisting of N₂ gas and Ar gas.

50. The method of claim 47, wherein the inert gas is one selected from the group consisting of N₂ gas and Ar gas.

51. The method of claim 48, wherein the inert gas is one selected from the group consisting of N₂ gas and Ar gas.

52. The method of claim 27, wherein the silicon atomic layer is formed by reacting with a silicon source gas.

53. The method of claim 52, wherein the silicon source gas is one selected from the group consisting of SiH₄ gas, Si₂H₆ gas, (CH₃)₃SiC≡CSi(CH₃)₃ gas, ((CH₃)₃Si)₂CH₂ gas, (CH₃)₃CSi(CH₃)₂Cl gas, (C₄H₉)SiCl₃ gas, (CH₃)₃SiN(C₂H₅)₂ gas, (CH₃)₂SiCl₂ gas, ((CH₃)₂Si-)_n gas, (C₆H₅)₂SiCl₂ gas, (C₆H₅)₂SiH₂ gas, C₂H₅SiCl₃ gas, Cl₃SiSiCl₃ gas, (CH₃)₃SiSi(CH₃)₃ gas, CH₃SiCl₂H gas, (CH₃)(C₆H₅)SiCl₂ gas, C₆H₅SiCl₃ gas, SiBr₄ gas,

SiCl₄ gas, SiF₄ gas, SiI₄ gas, (C₃₂H₁₆N₈)SiCl₂ gas, Si(Si(CH₃)₃)₄ gas, Si(CH₃)₄ gas, CH₃SiCl₃ gas, HSiCl₃ gas, (C₂H₅)₃SiCl gas, CF₃Si(CH₃)₃ gas, (CH₃)₃SiCl gas, (CH₃)₃SiH gas, (CH₃)₃SiC≡CH gas, (C₅H₅)Si(CH₃)₃ gas, (C₅(CH₃)₅)Si(CH₃)₃ gas, (C₆H₅)₃SiCl gas, (C₆H₅)₃SiH gas, ((CH₃)₂N)₃CH gas and CH₂=CHSiCl₃ gas.

54. The method of claim 27, further comprising the step of annealing at a predetermined temperature, after alternately stacking the metal atomic layer and the silicon atomic layer on the semiconductor substrate.

55. The method of claim 54, wherein the annealing is performed through one selected from the group consisting of a rapid thermal process, a furnace annealing process and vacuum thermal treatment.

56. A method for forming a metal silicide layer, comprising the steps of: forming a silicon atomic layer on a semiconductor substrate; forming a sacrificial metal atomic layer on the silicon atomic layer; removing the sacrificial metal atomic layer and concurrently forming a metal atomic layer on the semiconductor substrate by reacting the sacrificial metal atomic layer with a metal halide gas; and alternately stacking a plurality of silicon atomic layers and a plurality of metal atomic layers on the semiconductor substrate by forming the silicon atomic layer, the sacrificial metal atomic layer and the metal atomic layer in sequence at least once.

57. A method for forming a metal silicide layer substantially as shown in and/or described with reference to any of Figures 1, 2 and 5 to 7 of the accompanying drawings.

58. A method for forming a metal silicide layer substantially as shown in and/or described with reference to any of Figures 3 to 7 of the accompanying drawings.



The
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Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:
UK Cl (Ed.P): H1K(KHABX,KHAX,KHAC);C7F(FACE,FACX,FAXE,FAXX,FCD)
Int Cl (Ed.6): H01L
Other: ON LINE,W.P.I.

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
A	US5405806 MOTOROLA	

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X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
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